

REMARKS

Reconsideration of this application is respectfully requested in view of the foregoing amendments and the following remarks.

By the foregoing amendment, claims 10 and 11 have been canceled without prejudice or disclaimer for filing in a continuation application, and claims 1, 15 and 16 have been amended. The subject matter of the amendments to claims 1, 15 and 16 can be found in the specification as filed and in canceled claims 10 and 17. Thus, no new matter has been added. Claims 1-9, 12-13 and 15-16 are currently pending in the application and subject to examination.

In the Office Action mailed January 31, 2006, under 35 U.S.C. § 103(a), the Examiner rejected claims 1, 2, 4, 6-9, 12, 13 and 15 as being unpatentable over United States Patent No. 6,522,356 B1 to Watanabe (hereinafter "Watanabe") in view of United States Patent No. 5,592,575 to Nakazato (hereinafter "Nakazato") and further in view of United States Patent No. 6,900,838 B1 to Fujimura (hereinafter "Fujimura"), claims 3 and 5 as being unpatentable over Watanabe and Nakazato in view Fujimura and further in view of United States Patent No. 5,280,347 to Shiraishi (hereinafter "Shiraishi"), claims 10 and 11 as being unpatentable over Watanabe and Nakazato in view of Fujimura and further in view of United States Patent No. 5,663,759 to Horng (hereinafter, "Horng"), claim 16 as being unpatentable over Watanabe and Nakazato, in view of Fujimura and further in view of United States Patent Application Publication No. 2002/0076114 to Prakash (hereinafter, "Prakash"). It is noted that claims 10 and 11 have been canceled, and claims 1, 15 and 16 have been amended. To the extent that

the rejections remain applicable to the claims currently pending, the Applicant hereby traverses the rejections, as follows.

In making this rejection, the Examiner admits that none of Watanabe, Nakazato and Fujimura discloses or suggests a second signal-processing unit for generating output pixel signals by performing interpolation. Horng is applied as allegedly curing the deficiencies of the above combination. However, neither the vertical nor the horizontal interpolator of Horng generates output pixel signals at interstitial positions of said color pixels by performing interpolation processes using signals based on pixel signals of said number of color pixels, the interstitial positions and the color pixel positions constituting a tetragonal lattice, as recited in claim 1, as amended. Shirashi and Kang are not cited for, nor do they disclose at least this feature of amended claim 1.

Thus, the Applicant submits that none of the cited art of record, nor combination thereof, discloses or suggests at least the combination of a first signal processing unit for generating output pixel signals at at least some of said color pixels by using signals based on said pixel signals, said first signal processing unit generating a part of each output pixel signal directly from signals based on pixel signals of the color pixels of said one kind and generating another part of each output pixel signal through an interpolation process using signals based on pixel signals of color pixels of another of said at least three kinds, wherein, for each output pixel signal, the color pixels used in the interpolation process are located in rows above and below a row containing the color pixels of said one kind used in generating said output pixel signal, respectively; and a second signal processing unit for generating output pixel signals at interstitial positions of said color pixels by performing interpolation processes using signals based

on pixel signals of said number of color pixels, the interstitial positions and the color pixel positions constituting a tetragonal lattice, as recited in claim 1, as amended.

For at least this reason, the Applicant submits that claim 1, as amended, is allowable over the cited art of record. As claim 1 is allowable, the Applicant submits that claims 2-9, 12-13 and 15-16, each of which depends from allowable claim 1, are likewise allowable.

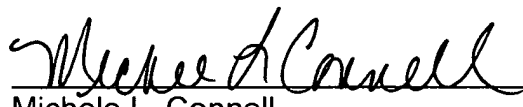
CONCLUSION

For all of the above reasons, it is respectfully submitted that the claims now pending patentability distinguish the present invention from the cited references. Accordingly, reconsideration and withdrawal of the outstanding rejections and an issuance of a Notice of Allowance are earnestly solicited.

Should the Examiner determine that any further action is necessary to place this application into better form, the Examiner is encouraged to telephone the undersigned representative at the number listed below.

In the event this paper is not considered to be timely filed, the Applicant hereby petitions for an appropriate extension of time. The Commissioner is hereby authorized to charge any fee deficiency or credit any overpayment associated with this communication to Deposit Account No. 01-2300, referencing docket number **107317-00038**.

Respectfully submitted,
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